

Part Number: WP1503EB/2ID

High Efficiency Red

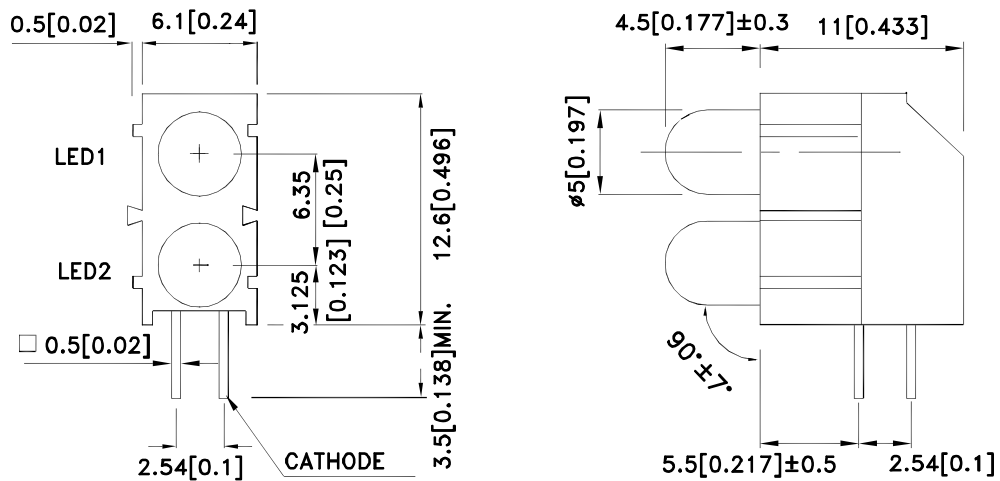
Features

- Pre-trimmed leads for pc board mounting.
- Stackable units.
- Colors can be mixed in a single housing.
- Black case enhances contrast ratio.
- Wide viewing angle.
- High reliability - life measured in years.
- Housing UL rating:94V-0.
- Housing material: type 66 nylon.
- RoHS compliant.

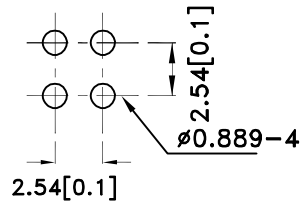
Description

The High Efficiency Red source color devices are made with Gallium Arsenide Phosphide on Gallium Phosphide Orange Light Emitting Diode.

Package Dimensions



RECOMMENDED PCB LAYOUT



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is $\pm 0.25(0.01)$ unless otherwise noted.
3. Lead spacing is measured where the leads emerge from the package.
4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.



Selection Guide

Part No.	Dice	Lens Type	Iv (mcd) [2] @ 10mA		Viewing Angle [1]
			Min.	Typ.	2θ1/2
WP1503EB/2ID	High Efficiency Red (GaAsP/GaP)	Red Diffused	25	50	60°

Notes:

1. θ1/2 is the angle from optical centerline where the luminous intensity is 1/2 of the optical peak value.
2. Luminous intensity/ luminous Flux: +/-15%.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Device	Typ.	Max.	Units	Test Conditions
λ _{peak}	Peak Wavelength	High Efficiency Red	627		nm	I _F =20mA
λ _D [1]	Dominant Wavelength	High Efficiency Red	625		nm	I _F =20mA
Δλ _{1/2}	Spectral Line Half-width	High Efficiency Red	45		nm	I _F =20mA
C	Capacitance	High Efficiency Red	15		pF	V _F =0V;f=1MHz
V _F [2]	Forward Voltage	High Efficiency Red	2	2.5	V	I _F =20mA
I _R	Reverse Current	High Efficiency Red		10	uA	V _R = 5V

Notes:

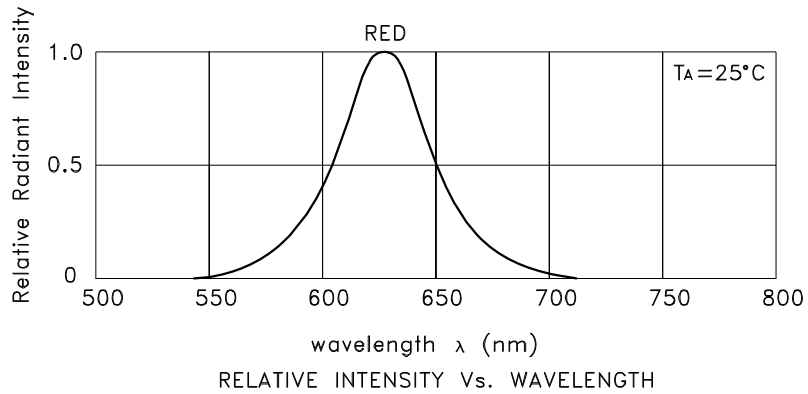
1. Wavelength: +/-1nm.
2. Forward Voltage: +/-0.1V.

Absolute Maximum Ratings at TA=25°C

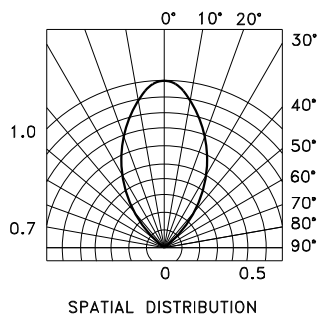
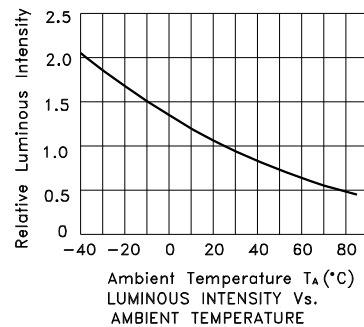
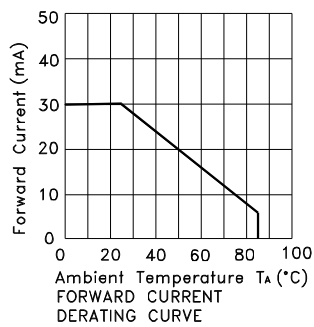
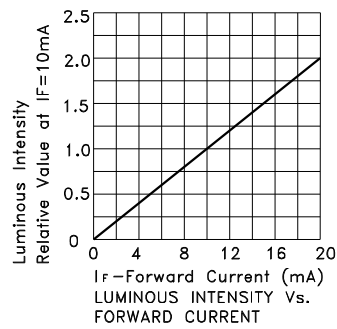
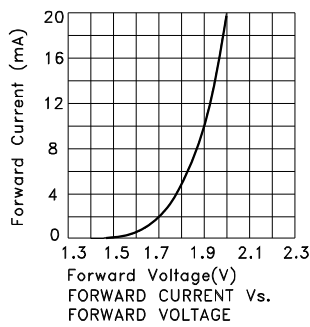
Parameter	High Efficiency Red	Units
Power dissipation	75	mW
DC Forward Current	30	mA
Peak Forward Current [1]	160	mA
Reverse Voltage	5	V
Operating/Storage Temperature	-40°C To +85°C	
Lead Solder Temperature [2]	260°C For 3 Seconds	
Lead Solder Temperature [3]	260°C For 5 Seconds	

Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.
2. 2mm below package base.
3. 5mm below package base.

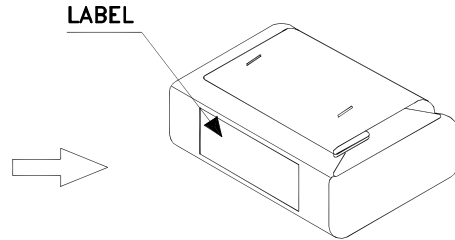
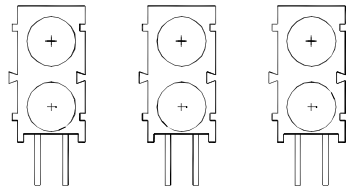


High Efficiency Red WP1503EB/2ID



PACKING & LABEL SPECIFICATIONS

WP1503EB/2ID

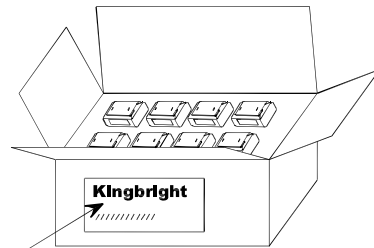


250PCS / BAG



12K / 9# BOX

OUTSIDE LABEL



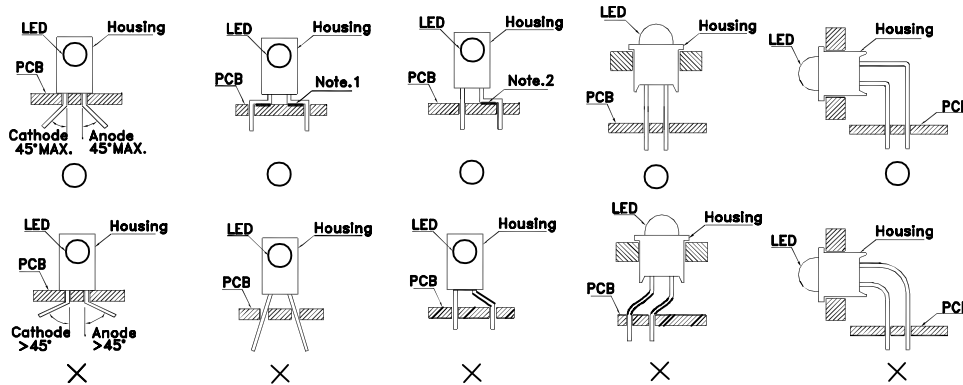
6K / 5# BOX

OUTSIDE LABEL

<h1>Kingbright</h1>				
P/NO: WP1503EBxxx				
QTY: 250 pcs	Q.C.			
S/N: XXXX	<table border="1"> <tr> <td style="text-align: center;">Q C</td> </tr> <tr> <td style="text-align: center;">XX XX XXXX</td> </tr> <tr> <td style="text-align: center;">PASSED</td> </tr> </table>	Q C	XX XX XXXX	PASSED
Q C				
XX XX XXXX				
PASSED				
CODE: XXX				
LOT NO:				
<small>XXXXXXXXXXXXXXXXXXXX</small> RoHS Compliant				

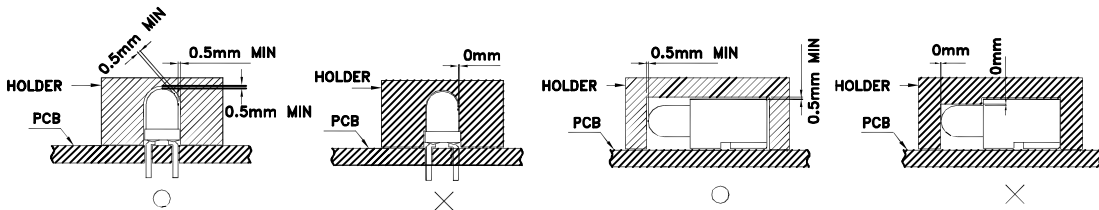
PRECAUTIONS

- The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures.

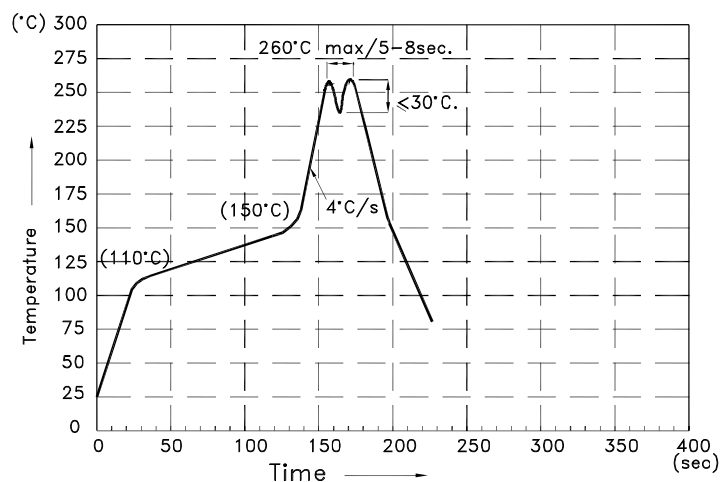


”○” Correct mounting method ”×” Incorrect mounting method

- During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering.



- The tip of the soldering iron should never touch the lens epoxy.
- Through-hole LEDs are incompatible with reflow soldering.
- If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.
- Recommended Wave Soldering Profile for Kingbright Thru-Hole Products



NOTES:

- Recommend the wave temperature 245°C~260°C. The maximum soldering temperature should be less than 260°C.
- Do not apply stress on epoxy resins when temperature is over 85°C.
- The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
- During wave soldering, the PCB top-surface temperature should be kept below 105°C.
- No more than once.